

a rewrite program area for storing a program for a rewriting processing procedure for said memory; and

a controller for forming a plurality of flag areas locally in said memory when the rewriting program is written into said memory, performing determination of completion of one or more stages of rewriting processing and recording results of the determination into the respective flag areas,

wherein the controller further determines if the rewriting processing was performed without interruption by comparing a value read from a flag area to an expected flag value, whereby if the controller determines that the rewriting processing was interrupted, resuming the rewriting processing at the stage at which the rewriting processing was interrupted.

3. (Amended) A computer provided with a memory and having a self-programming function of rewriting a program stored in said memory, comprising:

a rewrite program area for storing a program for a rewriting processing procedure for said memory;

rewriting means for forming a plurality of flag areas locally in said memory when the rewriting program is written into said memory; and

a controller for performing determination of completion of one or more stages of rewriting processing and recording results of the determination into the respective flag areas through said rewriting means,

AMENDMENT UNDER 37 C.F.R. § 1.116
U.S. APPLICATION NO. 09/401,293

wherein the controller further determines if the rewriting processing was performed without interruption by comparing a value read from a flag area to an expected flag value, whereby if the controller determines that the rewriting processing was interrupted, resuming the rewriting processing at the stage at which the rewriting processing was interrupted.

4. (Twice Amended) A computer provided with a memory and having a self-programming function of rewriting a program stored in said memory, comprising:

a rewrite program area for storing a program for a rewriting processing procedure for said memory;

rewriting means for forming a plurality of flag areas locally in said memory when the rewriting program is written into said memory;

a controller for performing determination of completion of one or more stages of rewriting processing and recording results of the determination into the respective flag areas through said rewriting means; and

flag state notification means for comparing, when power supply is made available after the rewriting is completed, values read out from said flag areas with expected values for said flag areas stored in advance and notifying said controller of results of the comparison,

wherein the controller determines if the rewriting processing was performed without interruption based on the results of the flag state notification means, whereby if the controller

AMENDMENT UNDER 37 C.F.R. § 1.116
U.S. APPLICATION NO. 09/401,293

62 determines that the rewriting processing was interrupted, resuming the rewriting processing at the stage at which the rewriting processing was interrupted.

7. (Twice Amended) A method of storing a program into a memory of a computer provided with said memory and having a self-programming function of rewriting the program stored in said memory, wherein

3 a plurality of flag areas are formed locally in said memory when a rewriting program is written into said memory, and determination of completion of one or more stages of rewriting processing is performed, whereafter results of the determination are recorded into the respective flag areas,

wherein the method further determines if the rewriting processing was performed without interruption by comparing a value read from a flag area to an expected flag value, whereby if a controller determines that the rewriting processing was interrupted, resuming the rewriting processing at the stage at which the rewriting processing was interrupted.